



FIG. 1

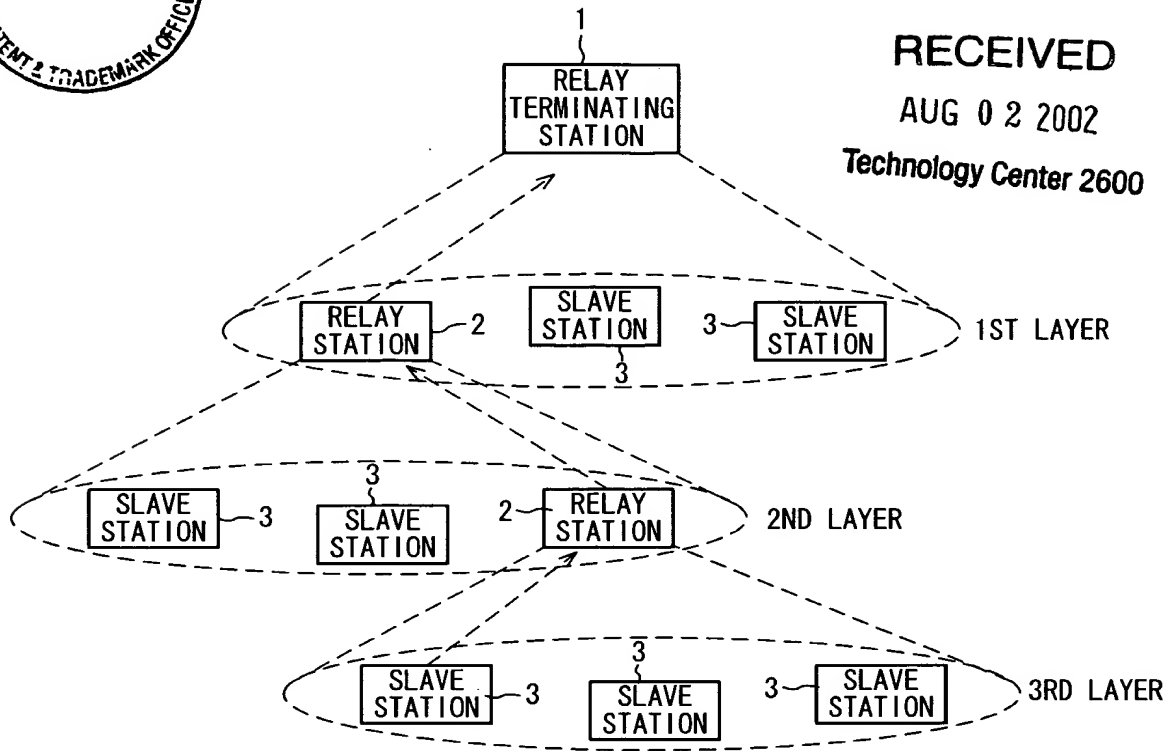


FIG. 2A

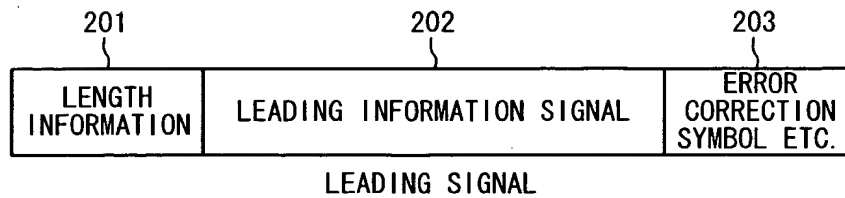


FIG. 2B

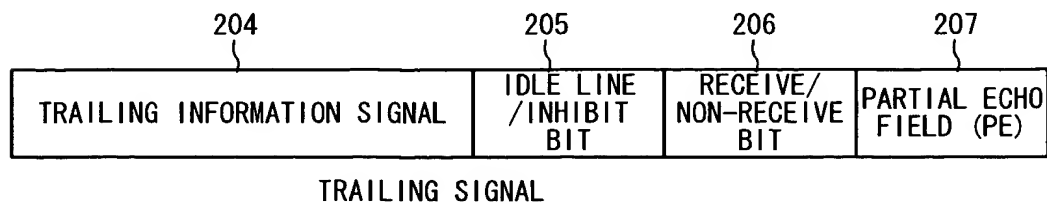
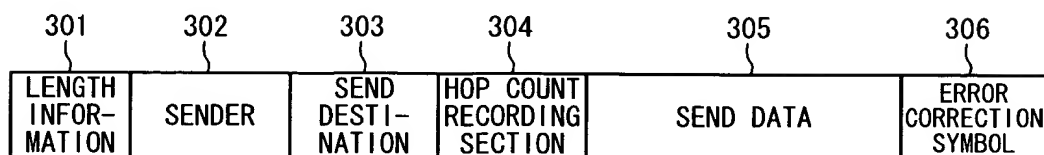




FIG. 3



RECEIVED

AUG 02 2002

Technology Center 2600

FIG. 4

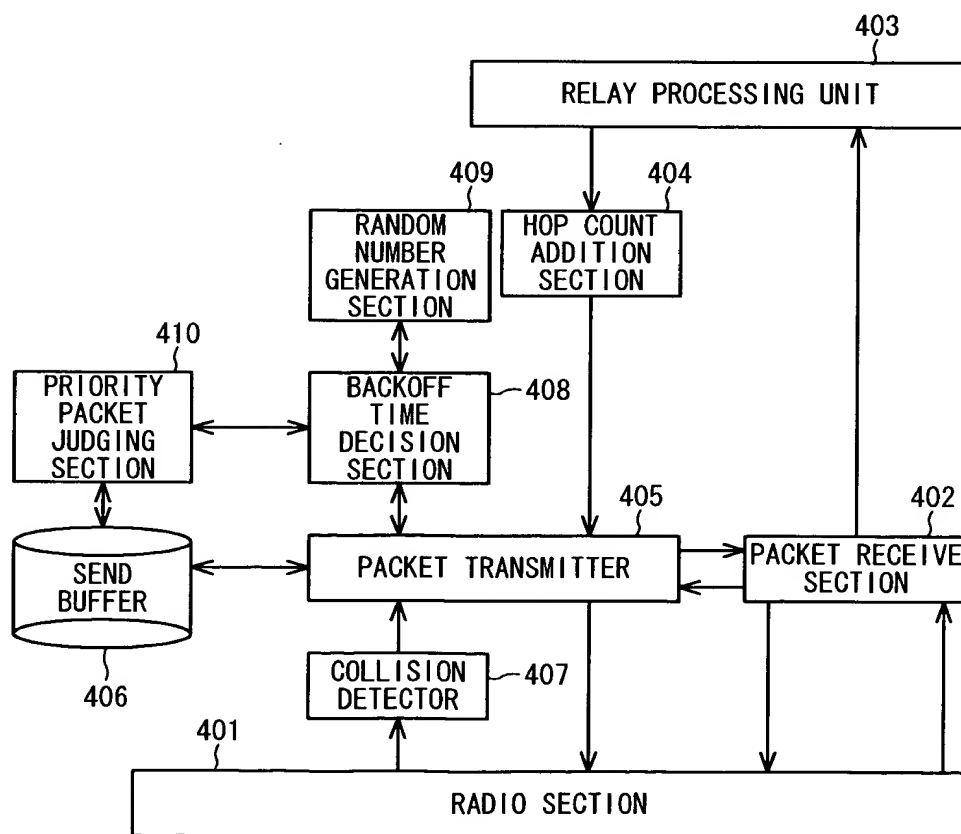




FIG. 5

RECEIVED

AUG 0 2 2002

Technology Center 2600

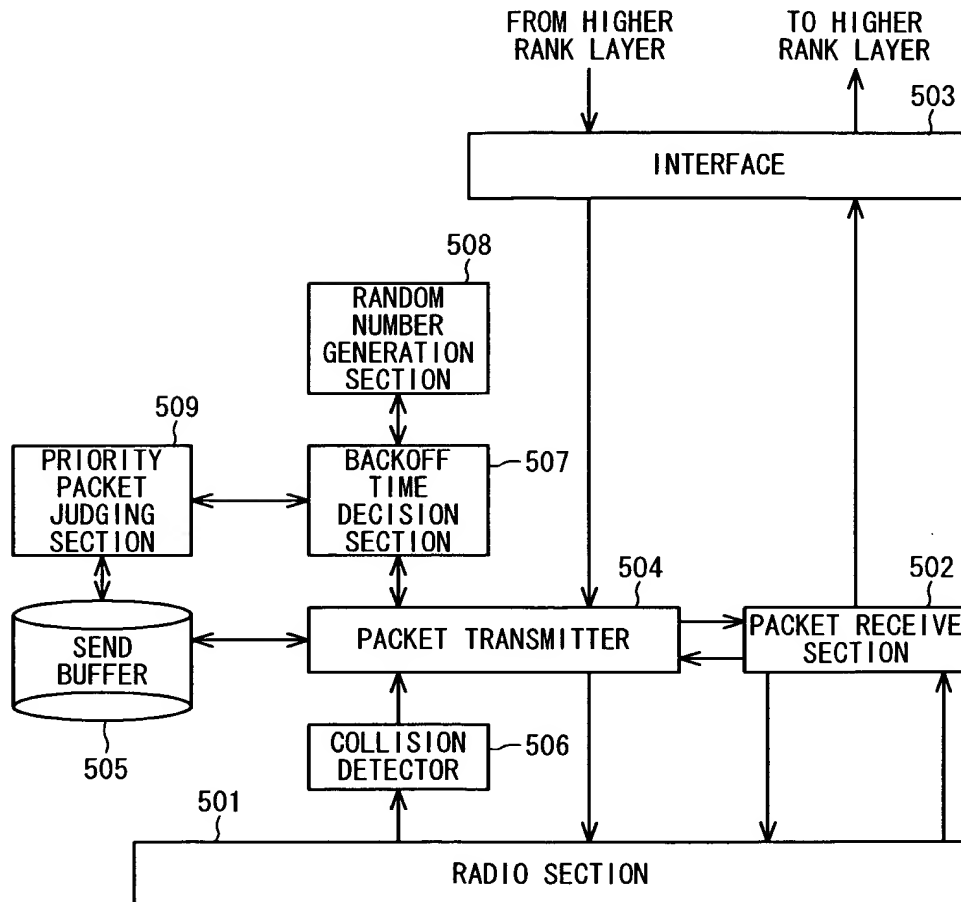
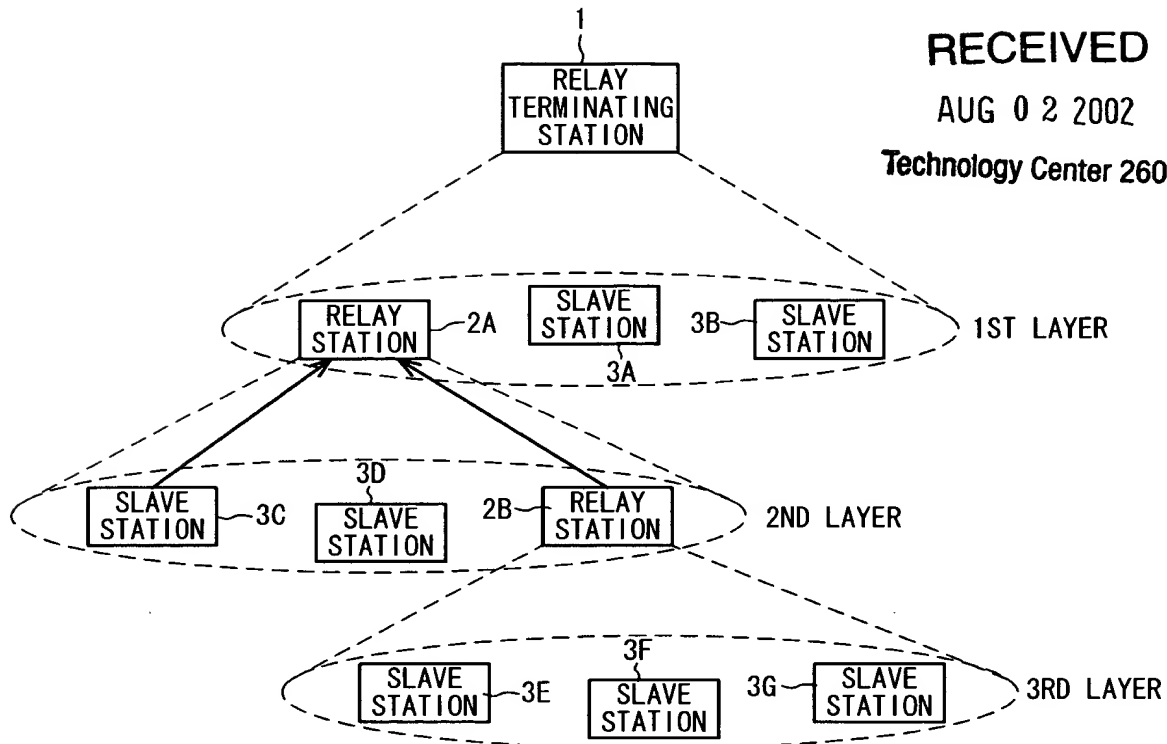




FIG. 6



RECEIVED

AUG 02 2002

Technology Center 2600



FIG. 7

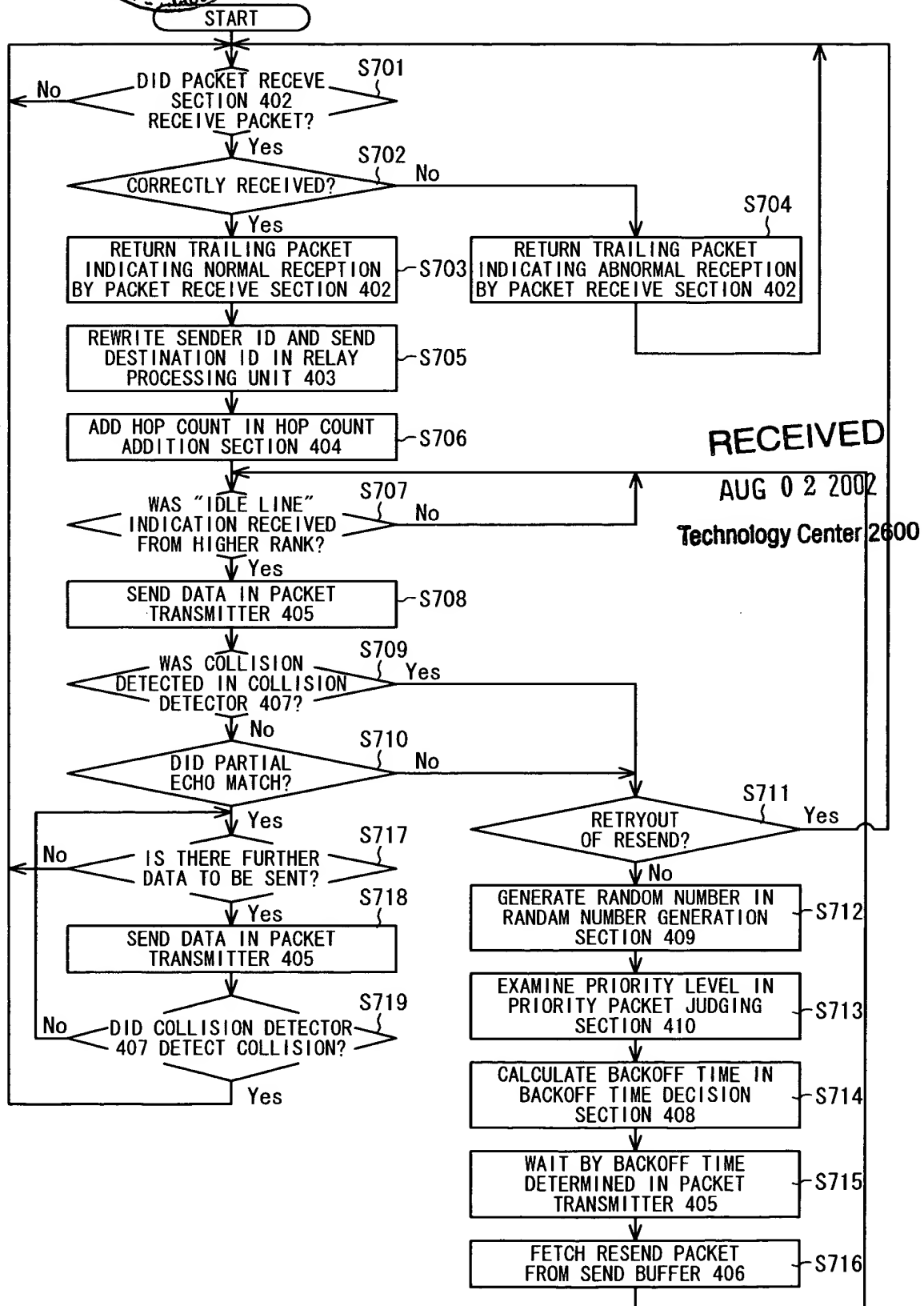




FIG. 8

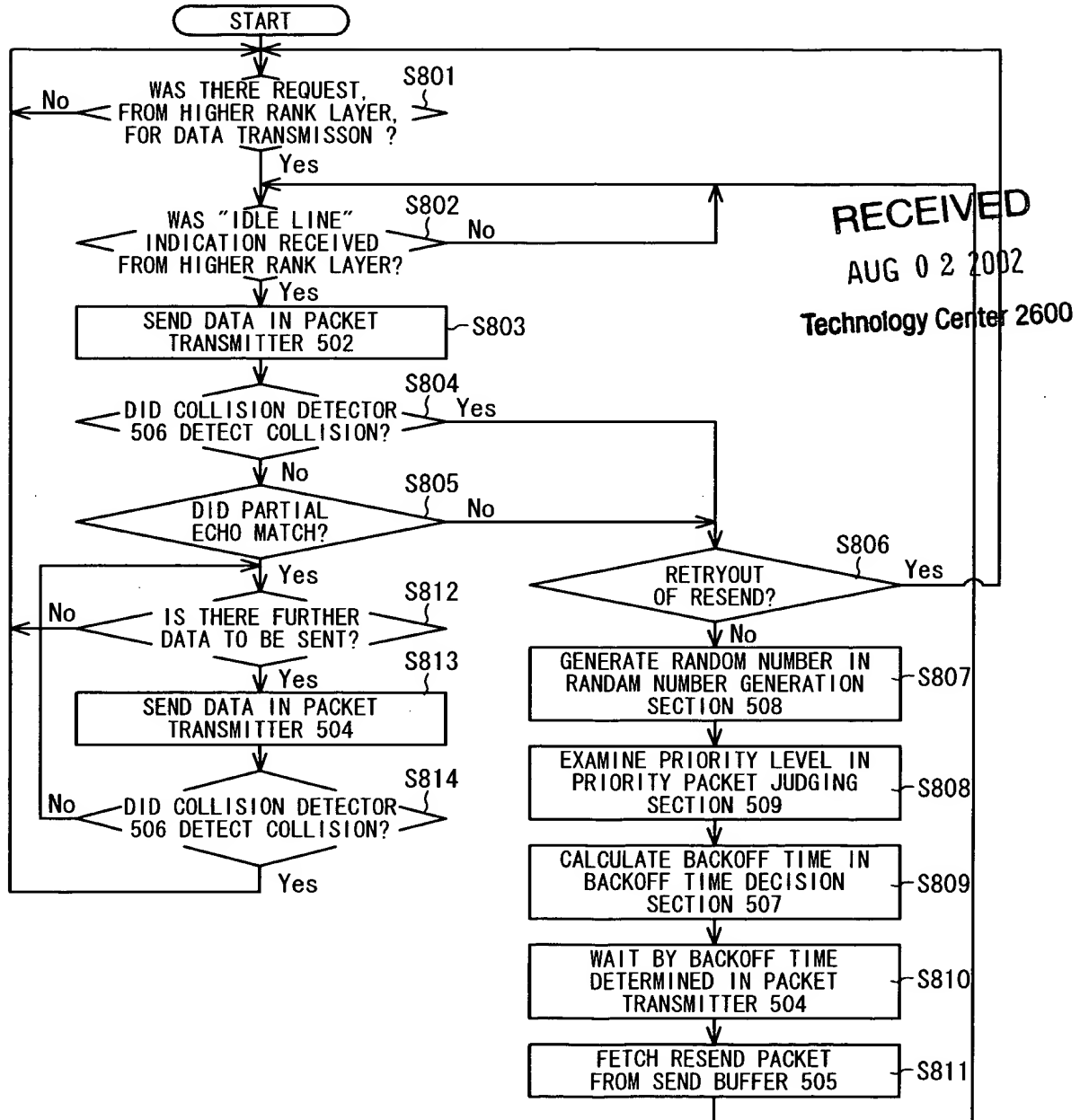


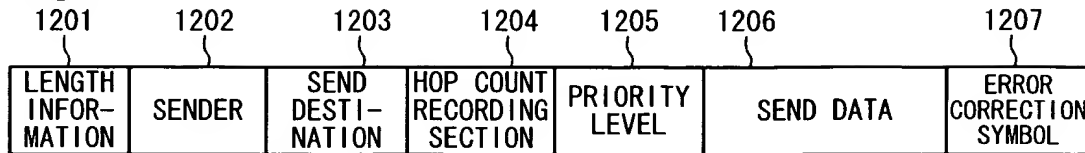
FIG. 1 is a block diagram of a packet relay system. The system includes a RADIO SECTION (901) at the bottom, which connects to a PACKET TRANSMITTER (904) and a PACKET RECEIVE SECTION (902). The PACKET TRANSMITTER (904) is connected to a SEND BUFFER (905) and a COLLISION DETECTOR (906). The PACKET RECEIVE SECTION (902) is connected to a RELAY PROCESSING UNIT (903). The RELAY PROCESSING UNIT (903) is connected to a BACKOFF TIME DECISION SECTION (907) and a RANDOM NUMBER GENERATION SECTION (908). The BACKOFF TIME DECISION SECTION (907) is connected to a PRIORITY PACKET JUDGING SECTION (909). The PRIORITY PACKET JUDGING SECTION (909) is connected to the SEND BUFFER (905). A stamp in the top left corner reads "AUG 01 2002" and "PATENT & TRADEMARK OFFICE". A stamp in the top right corner reads "RECEIVED AUG 02 2002 Technology Center 2600".

1001	1002	1003	1004	1005	1006
LENGTH INFORMATION	SENDER	SEND DESTINATION	PRIORITY LEVEL	SEND DATA	ERROR CORRECTION SYMBOL

1101	1102	1103	1104	1105	1106	1006
LENGTH INFORMATION	SENDER	SEND DESTINATION	CONTINUED BIT	DATA SIZE	SEND DATA	ERROR CORRECTION SYMBOL



FIG. 12



RECEIVED
 AUG 02 2002
 Technology Center 2600

FIG. 13

